Descriere HARDWARE [7]

Circuit de memorie EPROM care are doua functii de control , ambele trebuie sa fie active logic cu scopul de a obtine informatia la iesire.

Capacitatea de memorie 512Kbit sau 64Ko

Chip Enable (/E) is the power control and should be used for device selection.

Output Enable (/G) is the output control and should be used to gate data to the output pins, independent of device selection.

